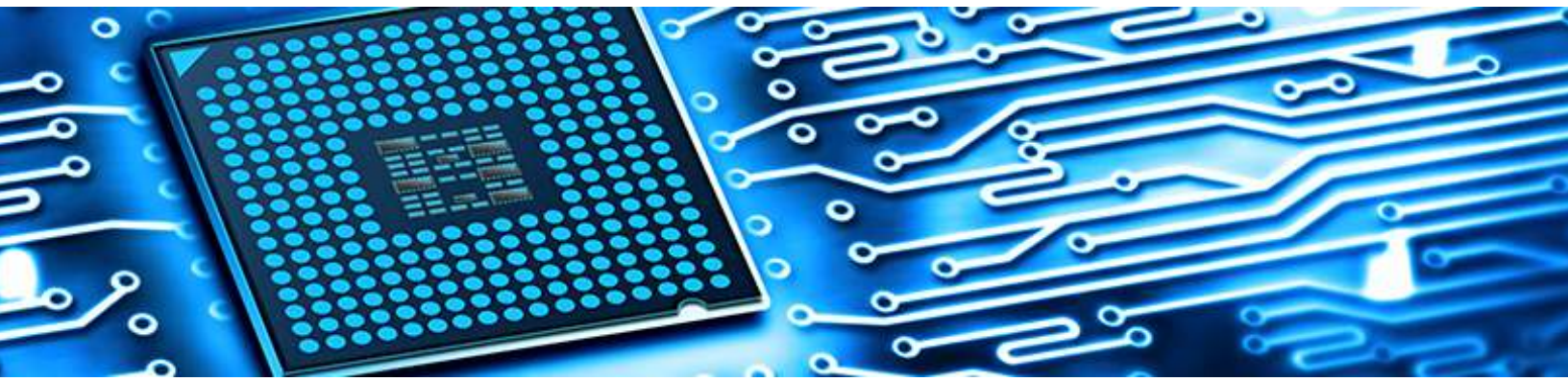


e NEX
Embedded & VLSI Training



Back-End Design



Introduction to VLSI	<p>Market Trends Applications of VLSI design Classification of IC technologies ASIC ,CPLD & FPGA EDA tools Types of VLSI design Techniques FPGA design</p>
Back End Design Overview	<p>Design Capabilities Low Power Needs & Advantages Low Power Design Methodologies Design Tools Available for Low Power</p>
	INTRODUCTION TO DSCH,MICROWIND,TANNER TOOLS
LAB on low power design	<p>INVERTER COUNTER MUX/DEMUX ENCODER/DECODER BCD MODULE</p>

Course Duration :

Eligibility :

Course Fee :

Batch Size :

