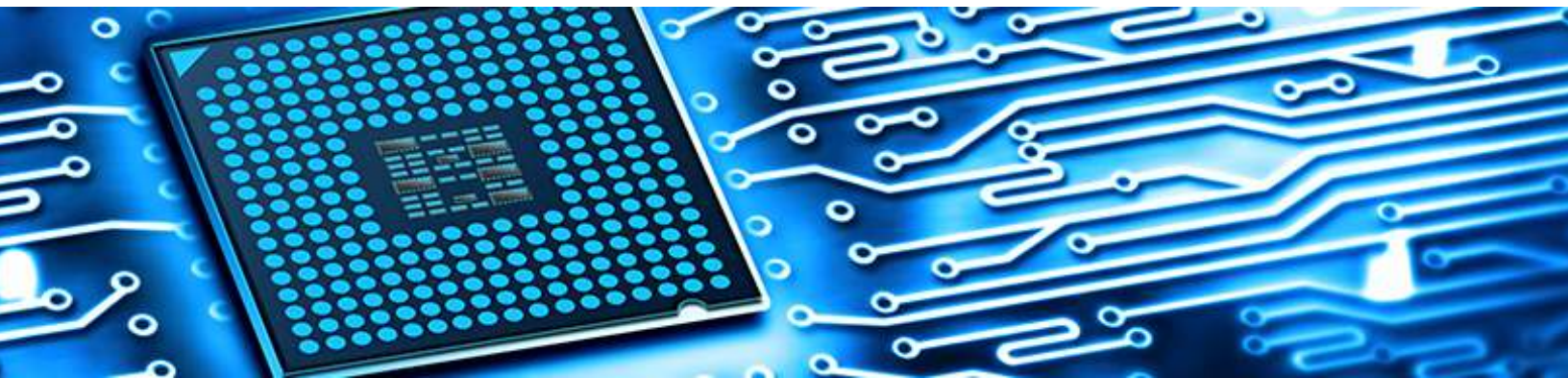




e NEX
Embedded & VLSI Training



VHDL



Introduction to VLSI	<p>Market Trends Applications of VLSI design Classification of IC technologies ASIC ,CPLD & FPGA EDA tools Types of VLSI design Techniques FPGA design</p>
Circuit Design with VHDL	<p>Introduction to VHDL Code structure Library Functions Entity Architecture Configuration Declaration Package Declaration</p>
Elements of VHDL LANGUAGES	<p>Different Data types Operators Attributes Generic Identifiers Variables & Signals</p>
Different types of VHDL Modeling	<p>Behavioral modeling Modeling techniques If statement Case statement Wait statement Loop statement Process statement</p>
Dataflow modeling	<p>When statements. Block statement Generate statement</p>
Structural Modeling	<p>Component declaration Component instantiation</p>
Test Bench	<p>Modeling a Test Bench Test Bench for Combinational Circuits Test Bench for Sequential Circuits</p>

Course Duration:

Eligibility :

Course Fee :

Batch Size :

